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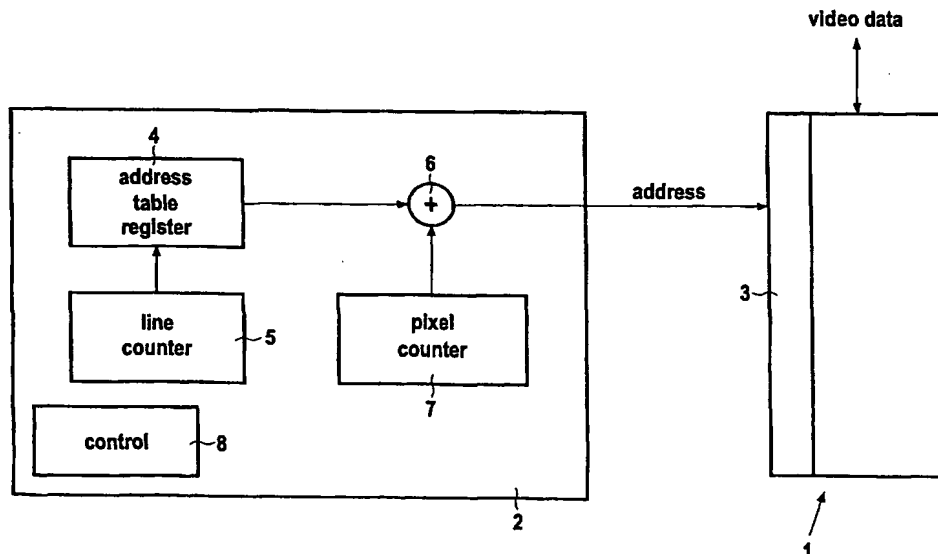
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**Declaration under Rule 4.17:**
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(54) **Title:** DRIVING METHOD, DRIVING CIRCUIT AND DRIVING APPARATUS FOR A DISPLAY SYSTEM

(57) **Abstract:** A table-based driving circuit for displays that switches between a normal operational mode and a read table block mode. The driving circuit comprises an address sequencer and a memory. The memory comprises the full table of individual sequences, such as interlacing or color-sequential sequence. In the read table mode, the next upcoming addresses are read, i.e. are downloaded, from the memory into an address table register in the address sequencer. In the normal operational mode, the address sequencer generates the addresses for the video data to be stored in the memory or to be displayed.